IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	John L. Melanson		
Assignee:	Cirrus Logic, Inc.		
Title:	Delta-Sigma Modulator with Integral Decimation		
Serial No.:	10/682,552	Filed:	October 9, 2003
Examiner:	Jean Bruner Jeanglaude	Group Art Unit:	2819
Docket No.:	1464-CA	Customer No.	33438

Austin, Texas
ELECTRONICALLY FILED August 21, 2006

37 C.F.R. § 1.114 RCE SUBMISSION

Dear Sir:

This paper is a submission in accordance with 37 C.F.R. § 1.114, which accompanies a request for continued examination in the above referenced patent application. Accompanying this submission is a petition under 37 C.F.R. § 1.136 for extension of time by two (2) months setting a new time for response of August 21, 2006 (August 20, 2006 being a Sunday). Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.